Roll No. | | | | | | | | | | | | | **Total No. of Pages : 02**

Total No. of Questions : 07

B.Sc.(IT) (2014 Batch) (Sem.–4)

MICROPROCESSOR SYSTEM

Subject Code : BS-206

M.Code : 12520

Time : 3 Hrs. **Max. Marks : 60**

INSTRUCTION TO CANDIDATES :

1. **SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.**
2. **SECTION-B contains SIX questions carrying TEN marks each and a student has to attempt any FOUR questions.**

SECTION-A

1. Answer briefly :

a) Difference between Primary Memory and Secondary Memory.

b) Explain T-State.

c) What is nibble?

d) What do we mean by unidirectional bus and bi-directional bus in 8085?

e) What do we mean by CARRY flag?

f) Name the flags that are affected after accomplishing the Arithmetic Operations.

g) List down the two major differences between HALT and HOLD state.

h) Define High Level Language (HLL) with an example.

i) What do we mean by BINARY data representation? Explain why it is necessary?

j) What is Tri-State logic?

SECTION-B

2. Illustrate the concept by citing how the binary addition and subtraction is carried out. Do explain the role of UNDERFLOW and OVERFLOW in data representation?

3. Explain the Organization of 8085 in relevance to Data /Address Bus in detail. List down the function of Accumulator and various flags in 8085.

4. What are semiconductor memories? Illustrate the concept of memories in detail with its characteristics.

5. What are INTERRUPTS? Explain in detail with respect to high priority interrupts along with an example.

6. What is Instruction? Classify it in terms of its word size in detail along with a suitable example.

7. Define :

   a) INTR Acknowledge machine cycle

   b) Error detection using Parity

   c) Memory and I/O Write Cycle.

   d) Register-Addressing Mode.

NOTE : Disclosure of Identity by writing Mobile No. or Making of passing request on any page of Answer Sheet will lead to UMC against the Student.